

Voltage Stability assessment by SVC Device Via CPF

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Abstract – one of the major causes of voltage (load) instability is the reactive power limit of the system. Improving the system's reactive power handling capacity via Flexible AC transmission system (FACTS) devices is a remedy for prevention of voltage instability and hence voltage collapse. In this paper, the effects of Static Var Compensator (SVC) in static voltage stability margin enhancement will be studied. Based on results at the point of collapse, design strategies are proposed for this device, so that its location dimensions can be optimally defined to increase system loadability. The IEEE14 bus test system is used to illustrate the application of SVC model and techniques.

Keywords – FACTS, SVC, CPFLOW, Voltage Stability.

I. INTRODUCTION

In recent years, the increase in peak load demand and power transfers between utilities has elevated concerns about system voltage security.

Voltage stability can also be called "load stability" [1]. Voltage stability is a problem in power systems which are heavily loaded, faulted or have a shortage of reactive power. The nature of voltage stability can be analysed by examining the production, transmission and consumption of reactive power. The problem of voltage stability concerns the whole power system, although it usually has a large involvement in one critical area of the power system.

The main factor causing voltage instability is the inability of the power system to meet the demands for reactive power in the heavily stressed systems to keep desired voltages. The loadability of a bus in the power system depends on the reactive power support that the bus can receive from the system. As the system approaches the maximum loading point or voltage collapse point, both real and reactive power losses increase rapidly. Therefore, the reactive power supports have to be local and adequate.

There are two types of voltage stability based on the time frame of simulation: static voltage stability and dynamic voltage stability. Static analysis involves only the solution of algebraic equations and therefore is computationally less extensive than dynamic analysis. Static voltage stability is ideal for the bulk of studies in which voltage stability limit for many pre-

contingency and post-contingency cases must be determined.

In static voltage stability, slowly developing changes in the power system occur that eventually lead to a shortage of reactive power and declining voltage. This phenomenon can be seen from the plot of the power transferred versus the voltage at receiving end. The plots are popularly referred to as P-V curve or "Nose" curve. As the power transfer increases, the voltage at the receiving end decreases. Eventually, the critical (nose) point, the point at which the system reactive power is short in supply, is reached where any further increase in active power transfer will lead to very rapid decrease in voltage magnitude. Before reaching the critical point, the large voltage drop due to heavy reactive power losses can be observed.

The only way to save the system from voltage collapse is to reduce the reactive power load or add additional reactive power prior to reaching the point of voltage collapse [2].

Voltage collapse phenomena in power systems have become one of the important concerns in the power industry over the last two decades, as this has been the major reason for several major blackouts that have occurred throughout the world including the recent Northeast Power outage in North America in August 2003 [3]. Point of collapse method and continuation method are used for voltage collapse studies [4].

Of these two techniques continuation power flow method is used for voltage analysis. These techniques involve the identification of the system equilibrium points or voltage collapse points where the related power flow Jacobian becomes singular [5, 6].

The flexible AC transmission system (FACTS) has received much attention in the last 2 decades. It uses high current power electronic devices to control the voltage, power flow, stability, etc. of a transmission system. FACTS devices can be connected to a transmission line in various ways, such as in series, shunt, or a combination of series and shunt. For example, the static VAR compensator (SVC) and static synchronous compensator (STATCOM) are connected in shunt; static synchronous series compensator (SSSC) and thyristor controlled series

capacitor (TCSC) are connected in series; thyristor controlled phase shifting transformer (TCPST) and unified power flow controller (UPFC) are connected in a series and shunt combination.

Canizares and Faur studied the effects of SVC and TCSC on voltage collapse [7]. Study of STATCOM and UPFC Controllers for Voltage Stability Evaluated by Saddle-Node Bifurcation Analysis is carry out in [8].

In this paper find the optimal location and rating of the SVC device and a continuation power flow is used to evaluate the effects of SVC on the system loadability for power systems.

Rest of the paper is organized as follows: Section II introduces the basic mathematical tools required for the analysis of voltage collapse phenomena.

FACTS modeling is presented in section III.

In section IV, some interesting results are presented along with detailed discussion finally; our contributions and conclusions are summarized in Section V.

II. VOLTAGE COLLAPSE

Voltage collapse studies and their related tools are typically based on the following general mathematical description of the system [9]:

$$\begin{aligned}\dot{\mathbf{x}} &= \mathbf{f}(\mathbf{x}, \mathbf{y}, \boldsymbol{\lambda}, \mathbf{p}) \\ 0 &= \mathbf{g}(\mathbf{x}, \mathbf{y}, \boldsymbol{\lambda}, \mathbf{p})\end{aligned}\quad (1)$$

where

$\mathbf{x} \in \mathcal{R}^n$ Represents the system state variables, corresponding to dynamical states of generators, loads, and any other time varying element in the system, such as FACTS controllers;

$\mathbf{y} \in \mathcal{R}^n$ Corresponds to the algebraic variables, usually associated to the transmission system and steady-state element models, such as some generating sources and loads in the network.

$\boldsymbol{\lambda} \in \mathcal{R}^k$ Stand for a set of uncontrolled parameters that drive the system to collapse, which are typically used to system demand.

Vector $\mathbf{p} \in \mathcal{R}^k$ is used here to represent system parameters that are directly controllable, such as shunt and series compensation levels.

Based on (1), the collapse point may be defined, under certain assumptions, as the equilibrium point where the related system Jacobian is singular, i.e., the

point $(\mathbf{x}_0, \mathbf{y}_0, \boldsymbol{\lambda}_0, \mathbf{p}_0)$ where

$$\begin{bmatrix} f(\mathbf{x}, \mathbf{y}, \boldsymbol{\lambda}, \mathbf{p}) \\ g(\mathbf{x}, \mathbf{y}, \boldsymbol{\lambda}, \mathbf{p}) \end{bmatrix} = \mathbf{F}(\mathbf{z}, \boldsymbol{\lambda}, \mathbf{p}) = 0 \quad (2)$$

And $\mathbf{D}_{\mathbf{z}} \mathbf{F}|_0$ has a zero eigenvalue (or zero singular value) [12]. This equilibrium is typically associated to a saddle-node bifurcation point [10].

For a given set of controllable parameters \mathbf{p}_0 , voltage collapse studies usually concentrate on determining the collapse or bifurcation point $(\mathbf{x}_0, \mathbf{y}_0, \boldsymbol{\lambda}_0)$, where $\boldsymbol{\lambda}_0$ typically corresponds to the maximum loading level or loadability margin in p.u., %, MW, Mvar or MVA, depending on how the load variations are modeled and defined. Based on bifurcation theory, two basic tools are typically applied to the computation of the collapse point, namely, direct and continuation methods.

In voltage collapse studies, the continuation method shows many advantages, so, most of the reaserchers apply this technique to trace voltage profile at various buses of the test power system, with respect to changes of loading level $\boldsymbol{\lambda}$, namely, Continuation Power Flow (CPF).

In this paper the continuation power flow algorithm with smooth changes of loading level at various buses of the system, is chosen for simulation purpose.

III. FACTS MODELING

Each model is described by a set of differential algebraic equations :

$$\begin{aligned}\dot{\mathbf{X}}_c &= \mathbf{f}(\mathbf{X}_c, \mathbf{X}_s, \mathbf{V}, \boldsymbol{\theta}, \mathbf{u}) \\ \dot{\mathbf{X}}_s &= \mathbf{f}(\mathbf{X}_c, \mathbf{X}_s, \mathbf{V}, \boldsymbol{\theta}) \\ \mathbf{P} &= \mathbf{g}_p(\mathbf{X}_c, \mathbf{X}_s, \mathbf{V}, \boldsymbol{\theta}) \\ \mathbf{Q} &= \mathbf{g}_q(\mathbf{X}_c, \mathbf{X}_s, \mathbf{V}, \boldsymbol{\theta})\end{aligned}\quad (3)$$

Where:

\mathbf{X}_c is the control system variable,

X_s is the controlled state variable (e.g firing angle)

V and θ are the voltage amplitude and phases at the buses at which the components are connected.

u represents the input control parameters, such as reference power flows. FACTS models are based on what was proposed in [11].

A. STATIC VAR COMPENSATION (SVC)

The two most popular configuration of this type of shunt controller are the fixed capacitor (FC) with a thyristor controlled reactor (TCR) and the thyristor switched capacitor (TSC) with TCR. Among these two setups, the second (TSC-TCR) minimizes stand-by losses; however from a steady-state point of view, this is equivalent to the FC-TCR. In this paper, the FC-TCR structure is used for analysis of SVC which is shown in figure 1.

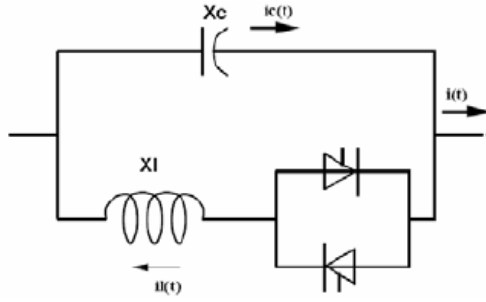


Fig. 1 Equivalent FC-TCR circuit of SVC.

The TCR consists of a fixed reactor of inductance L and a bi-directional thyristor valve that are fired symmetrically in an angle control range of 90° to 180° , with respect to the SVC voltage.

Assuming controller voltage equal to the bus voltage and performing a Fourier series analysis on the inductor current wave form, the TCR at fundamental frequency can be considered to act like variable inductance given by [7]:

$$X_v = X_L \frac{\pi}{2(\pi - \alpha) + \sin 2\alpha} \quad (4)$$

Where, X_L is the reactance caused by the fundamental frequency without thyristor control and α is the firing angle.

Hence, the total equivalent impedance of the controller can be represented as:

$$X_e = X_c \frac{\pi/r_x}{\sin 2\alpha + 2\alpha + \pi(2 - \frac{1}{r_x})} \quad (5)$$

Where $r_x = X_c/X_L$. The limits of the controller are given by the firing angle limits, which are fixed by design. The typical steady-state control law of a SVC used here is depicted in figure 2, and may be represented by the following voltage-current characteristic:

$$V = V_{ref} + X_{SL} I \quad (6)$$

Where V and I stand for the total controller RMS voltage and current magnitudes, respectively, and V_{ref} represent a reference voltage.

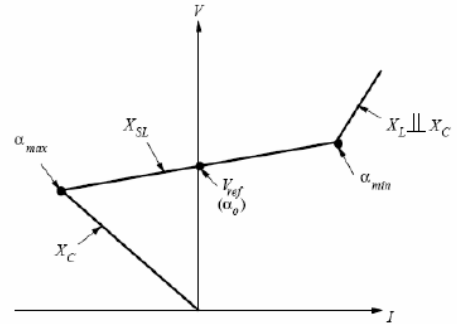


Fig. 2 Typical steady state V-I characteristic of a SVC.

Typical values for the slope X_{SL} are in the range of 2 to 5 %, with respect to the SVC base; this is needed to avoid hitting limits for small variations of the bus voltage. A typical value for the controlled voltage range is $\pm 5\%$ about V_{ref} [7]. At the firing angle limits, the SVC is transformed into a fixed reactance.

IV. SIMULATION RESULTS

A IEEE 14-bus test system as shown in figure 3 is used for voltage stability studies. The test system consists of five generators and eleven PQ bus (or load bus). The simulation use a PSAT simulation software [12]. PSAT is a power system analysis software,

which has many features including power flow and continuation power flow. Using continuation power flow feature of PSAT, voltage stability of the test system is investigated.

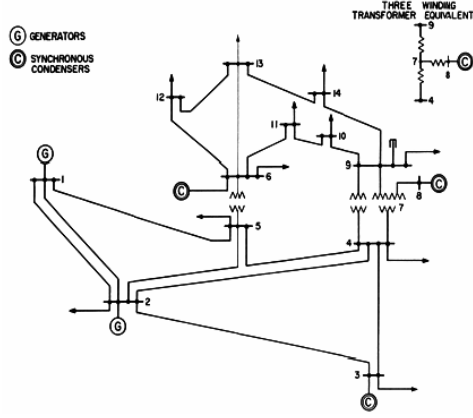


Fig. 3 The IEEE 14-bus test system

A. LOCATION OF SVC

The behaviour of the test system with and without SVC under different loading conditions is studied.

The location of the SVC device is determined through CPF technique. A typical PQ model is used for the loads and the generator limits are ignored.

Voltage stability analysis is performed by starting from an initial stable operating point and then increasing the loads by a factor λ until singular point of power flow linearization is reached. The loads are defined as

$$P_1 = \lambda P_0$$

$$Q_1 = \lambda Q_0$$

where P_0 and Q_0 are the active and reactive base loads, whereas P_1 and Q_1 are the active and reactive loads at bus 1 for the current operating point as defined by λ .

From the CPF analyses which are shown in the figure 4, the buses 4, 5, 9 and 14 are the critical buses. Figure 5 shows PV curves for the critical buses for 14-bus test system without SVC.

Maximum loading point or bifurcation point where the Jacobian matrix becomes singular occurs at $\lambda = 2.76$, bus 14 is indicated as the critical voltage bus needing Q support

Whereas figure 6 shows the P-V curves at bus 14 for different contingencies for the IEEE 14-bus test system.

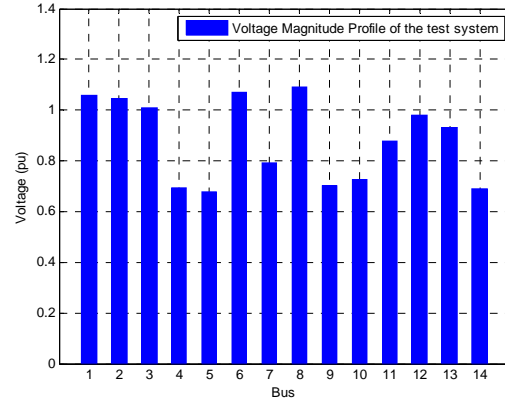


Fig. 4 Voltage Profile of IEE 14 Bus system

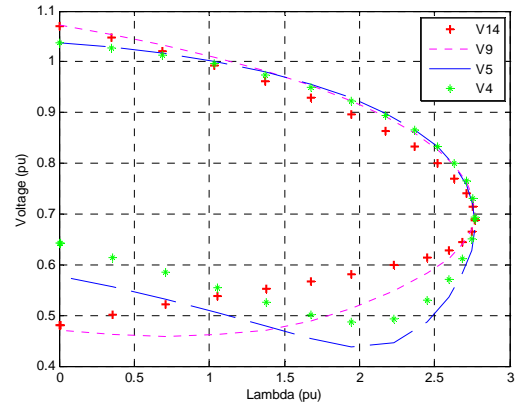


Fig. 5 PV curve for 14 bus system without SVC

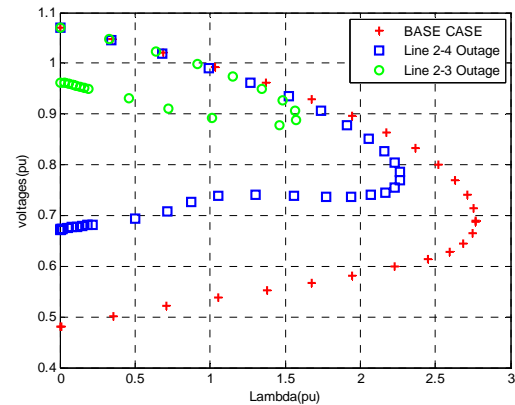


Fig. 6 PV curve at bus 14 for different contingencies

B. DESIGN OF SVC

After the optimal location of the SVC was determined to be at bus 14, the ratings of the SVC have to be chosen. It is to be expected that by introducing a SVC at that location. The voltage profiles will be flatter and the loadability margin of the system will increase. Assuming a typical control

range for the voltage at that bus, i.e., $\pm 5\%$, one can treat that bus as a PV bus with the voltage fixed at 0.95 p.u. This means placing a reactive power source, i.e; a synchronous compensator, at that bus. Considering that the SVC has to control the voltage at the loading level where the system without the SVC collapses, the required reactive power injection at that bus is found from the power flow. This is the maximum capacitive rating of the SVC.

If the inductive power rating of the SVC is considered equal to the capacitive rating at 1.05 p.u. bus voltage, then the inductive reactance

X_L of the SVC is 55% of the capacitive reactance X_C [11].

C. PV curve for the system

The voltage profiles for the system with the SVC at bus 14 are shown in figure 7. As expected, the bifurcation for the system with the SVC placed at bus 14 occurs at a higher load value, i.e., $\lambda = 2.85$.

Also, the voltage profiles are much flatter within the control limits of the SVC. Which are reached by design at approximately the previous bifurcation value $\lambda = 2.76$.

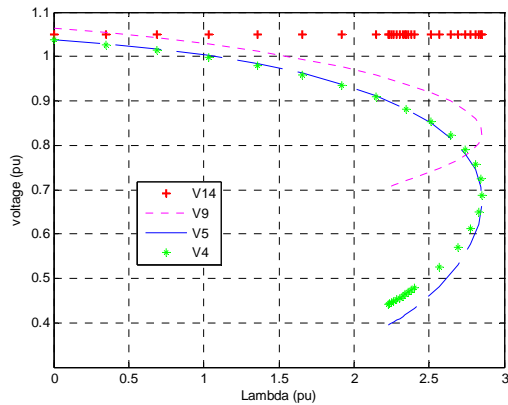


Fig. 7 PV curve for 14 bus system with SVC at bus 14

Voltages profiles of base case and system with SVC are illustrated in figure 8. It is obviously from figure 8 SVC provides a better voltage profile at the collapse point. This is due to the reason that the SVC is installed at the weakest bus.

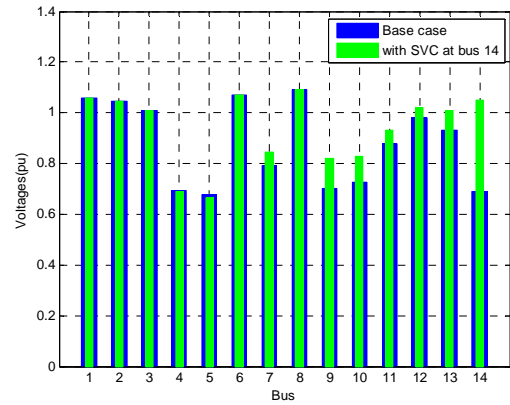


Fig. 8. Voltage Profile of system with SVC

The nose curves for the system with the same SVC located now at bus 9 are shown in figure 9. The controlled range of the voltages is considerably less than for the SVC placed at bus 14. Furthermore, the bifurcation occurs at a lower load value of $\lambda = 2.82$.

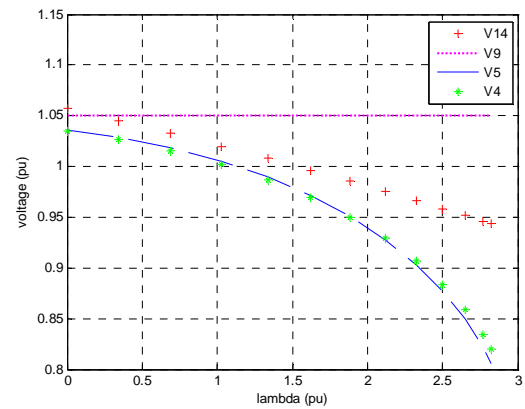


Fig. 9 PV curve for 14 bus system with SVC at bus 9

D. Voltages Profiles for line 2-3 outage

Voltages profiles at the collapse point for line 2-3 outage and system with SVC are illustrated in figure 10. From figure 10, SVC provides a better voltage profile at the collapse point.

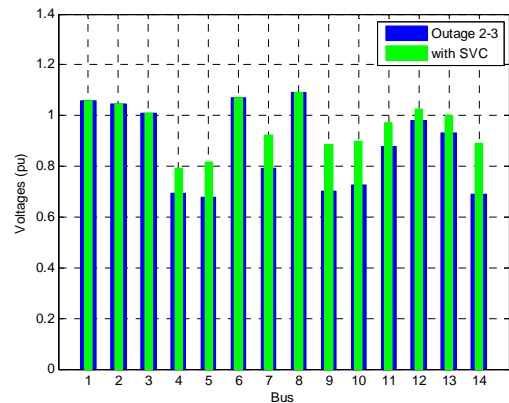


Fig. 10 Voltage Profile of line 2-3 outage with SVC

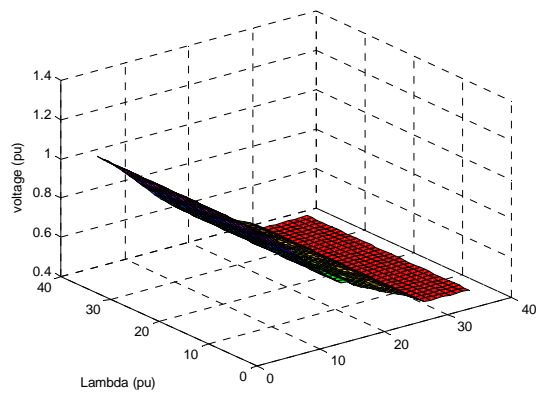


Fig.11. PV surface for bus number 14 of 14-bus test system without SVC

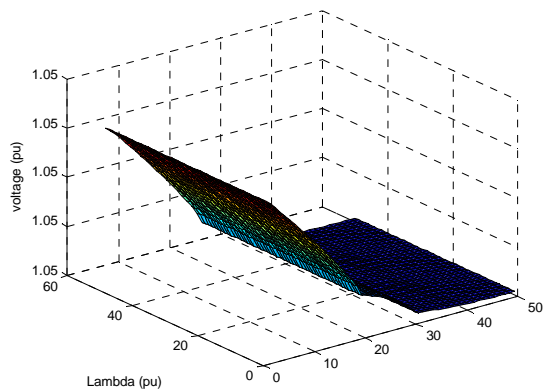


Fig. 12. PV surface for bus number 14 of 14-bus test system with SVC

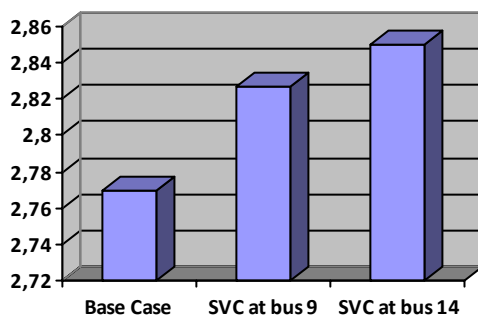


Fig. 13. Maximum loading point with SVC device

V. CONCLUSION

The results presented in this paper clearly show how SVC can be used to increase system loadability in practical power system. The test system requires reactive power the most at the weakest bus, which is located in the distribution level. Introducing reactive power at this bus using SVC can improve loading margin the most. In this paper adequate models for the SVC in the steady state studies are presented and thoroughly discussed. Hence, a technique to identify the optimal placement of the SVC device and related

equations are derived. The results of simulations on the IEEE 14 bus test system have clearly shown that how SVC device increased the buses voltage, power limits, line powers, and loading capability of the network.

V. APPENDIX

PV surface of bus 14 of the IEEE 14- bus test system is without and with SVC shown in figures 11-12.

From figure 13 it is obviously that the loading parameter of the system with SVC at bus 14 is highest while that without SVC is lowest.

VI. REFERENCES

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